

19=30  
~21:00

run 207  
~ 221.

DAQ 7/15/7u

現状 = EASTROC + CAMAC の同時 = 取得時  
 CAMAC ADC, TDC への外-付口  
 → look at me?  
 → file open 毎回作業  
 解決 = dreg-v4  
 仕様 Map ADC x 4 (6ch)  
 TDC x 1 (5ch)  
 EASTROC x 1 (6ch)